<u>PATENT</u>

Attorney Docket No.: 018865-001740US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MO et al.

Application No.: Not Yet Assigned

Filed: Herewith

For: FIELD EFFECT TRANSISTOR

AND METHOD OF ITS

**MANUFACTOR** 

Examiner: Not Yet Assigned

Art Unit: Not Yet Assigned

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 CFR §1.98(d), copies of the references can be found in Application No. 08/970,221, filed November 14, 1997 (Attorney Docket No. 018865-001700US) and Application No. 09/854,102, filed May 9, 2001 (Attorney Docket No. 018865-001710US). It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Mo et al. **PATENT** 

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Applicant believes that no fee is required for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Babak S. Sani Reg. No. 37,495

TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, 8<sup>th</sup> Floor San Francisco, California 94111-3834

Tel: 415-576-0200 Fax: 415-576-0300

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				Application Number	Not Yet Assigned	
INFO	RMATION	DIS	CLOSURE	Filing Date	Herewith	
STAT	<b>TEMENT B'</b>	Y Al	PPLICANT	First Named Inventor	MO et al.	
				Art Unit	Not Yet Assigned	
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Page	1	of	5	Attorney Docket Number	018865-001740US	

U.S. PATENT DOCUMENTS									
		Document Number							
Examiner	Cite No. <sup>1</sup>	Number Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				
	AA	4,070,690	01-24-1978	Wickstrom					
	AB	4,132,998	01-02-1979	Dingwall					
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Examiner	 Date	
Signature	Considered	

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			U.S. PATENT DO	DCUMENTS	
		Document Number			
Examiner	Cite No.'	Number Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	со	5,780,324	07-14-1998	Tokura, et al.	
	CP	5,783,491	07-21-1998	Nakamura et al.	

Examiner Signature Date Consider	ered
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					Application Number	Not Yet Assigned		
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	Herewith		
					First Named Inventor	MO et al.		
					Art Unit	Not Yet Assigned		
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	Page	3	of	5	Attorney Docket Number	018865-001740US		

			U.S. PATENT D	OCUMENTS	
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Examiner	Cite No. <sup>1</sup>	Number Kind Code <sup>2</sup> (if known)	Publication Date , MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	CR	5,814,858	09-29-1998	Williams	
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	CY	6,015,737	0118-2000	Tokura, et al.	
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	DD	EP	0 550 770	B1	07-14-1993			$\top$	
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	DG	EP	0 720 235	A2	07-03-1996			$\top$	
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PTO/SB/08A (04-03)

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Page	4	of	5	Attorney Docket Number	018865-001740US		

			FO	REIGN PA	TENT DOCUME	NTS		
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Page 5 of 5	Attorney Docket Number	018865-001740US

NON PATENT LITERATURE DOCUMENTS				
	i	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the		
		item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue	T <sup>2</sup>	
Examiner Initials *	Cite No. <sup>1</sup>	number(s), publisher, city and/or country where published.		
	EM	BLANCHARD, R.A., "Optimization of discrete high power MOS transistors," Stanford Electronics Laboratory, Integrated Circuits Laboratory, April 1982, Technical Report No. IDEZ696-2.		
	EN	CHANG, T.S. AND CRITCHLOW, D.L., "Verticle FET random-access memories with deep trench isolation," IBM Technical Disclosure Bulletin, January 1980, pp. 3683-3687, Vol. 22(8B).		
	EO	GRANT, D.A. AND GOWER, J., "The development of power MOS devices," Chapter 1.2 In Power Mosfets: Theory and applications. John Wiley & Sons; New York, 1989, pp. 5-23.		
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	ET	OU-YANG, P., "Double ion implanted V-MOS technology," IEEE Journal of Solid-State Circuits, February 1977, pp. 3-10, Vol SC-12(1).		
	EU	SALAMA, C.A. AND OAKES, J.G., "Nonplanar power field-effect transistors," <i>IEEE Transactions on Electron Devices</i> , October 1978, pp. 1222-1228, Vol. Ed-25(10).		
	EV	SUN, S.C., "Physics and technology of power MOSFETs," Stanford Electronics Laboratory, Integrated Circuits Laboratory, February 1982, Technical Report No. IDEZ696-1.		
	EW	SZE, S.M., "P-N- Junction diode," Chapter 2 In Physics of Semiconductor Devices. Second Edition, John Wiley & Sons; New York, 1981, pp. 63-108.		

Examiner	Date
Signature	Considered

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